

Form PTO-1449	Document Number (Sequence)	Application Number
OPEN INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)	TSMC-02-818	10/689,421
	Applicant: Denny D. Tang et al.	
	Filing Date: 10/20/03	Drawings Unit



U. S. PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JA	631737611/13/01		Tran et al.	365	210	6/20/00
JA	60551784/25/00		Naji	365	158	12/18/98
JA	61696891/2/01		Naji	365	173	12/8/99
JA	63851095/7/02		Naji	365	209	1/30/01
JA	64269077/30/02		Hoening Schmid	365	210	4/17/01
JA	64456129/3/02		Naji	365	158	8/27/01
JA	649643612/17/02		Naji	365	209	3/5/02

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Portfolios Pages, Etc.)

JA	{	"Demonstration of a Four State Sensing Scheme for a Single Pseudo-Spin Valve GMR Bit," Zhang et al., IEEE Trans. on Magnetics, Vol. 35, No. 5, Sept. 1999, pp. 2829-2831.
JA	{	"Windowed MRAM Sensing Scheme," Zhang et al., "Memory Tech., Records of the 2000 IEEE Int'l Workshop on Design and Testing," Aug. 2000, pp. 47-52.

EXAMINER <i>J. H. H.</i>	DATE CONSIDERED 1/26/05
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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TSMC-02-818

10/689,421


Applicant:

Denny D. Tang et al.

Filing Date

10/20/03

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OTHER DOCUMENTS (Including Author, Title, Date, Portion/ Pages, Etc.)	
9A	<p>- ("A Novel Sensing Scheme for an MRAM with a 50% MR Ratio Yamada et al., 2001 Symp. on VLSI Circuits Digest of Tech. Papers, June 2001, pp. 123- 124.</p>
9A	<p>- ("Fully Integrated 64 Kb MRAM with Novel Reference Cell Scheme," Jeong et al., Digest Int'l Electron Devices Meet IEDM'02 Dec. 2002, pp. 551-554.</p>

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FOREIGN PATENT DOCUMENTS

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COUNTRY

CLASS

SUBCLASS

Translation

YES	NO
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

U.S. Patent Application Publication US 2001/0053104 A1
to Tran et al., "Reference Signal Generation for
Magnetic Random Access Memory Devices", Pub. Date 12/20/01,
Filed 3/14/01, U.S.C. 365/210.

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